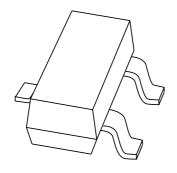
DISCRETE SEMICONDUCTORS

DATA SHEET



BC859; BC860 PNP general purpose transistors

Product specification Supersedes data of 1998 Jul 16 1999 May 28





PNP general purpose transistors

BC859; BC860

FEATURES

• Low current (max. 100 mA)

• Low voltage (max. 45 V).

APPLICATIONS

• Low noise input stages of audio frequency equipment.

DESCRIPTION

PNP transistor in a SOT23 plastic package. NPN complements: BC849 and BC850.

MARKING

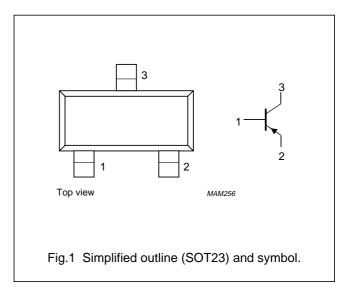
TYPE NUMBER	MARKING CODE ⁽¹⁾	TYPE NUMBER	MARKING CODE ⁽¹⁾	
BC859B	4B*	BC860B	4F*	
BC859C	4C*	BC860C	4G*	

Note

* = p : Made in Hong Kong.
 * = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BC859		_	-30	V
	BC860		_	-50	V
V _{CEO}	collector-emitter voltage	open base			
	BC859		_	-30	V
	BC860		_	-45	V
V _{EBO}	emitter-base voltage	open collector	_	- 5	V
I _C	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

PNP general purpose transistors

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	$I_E = 0; V_{CB} = -30 \text{ V}$	_	-1	-15	nA
		$I_E = 0$; $V_{CB} = -30 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	_	_	-4	μΑ
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = -5 V	_	_	-100	nA
h _{FE}	DC current gain BC859B; BC860B BC859C; BC860C	$I_C = -2 \text{ mA}$; $V_{CE} = -5 \text{ V}$; see Figs 2 and 3	220 420	_	475 800	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-75 -250	-300 -650	mV mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -100 \text{ mA}; I_B = -5 \text{ mA}$ $I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}; \text{ note 1}$ $I_C = -100 \text{ mA}; I_B = -5 \text{ mA}; \text{ note 1}$		-250 -700 -850	-000	mV mV
V _{BE}	base-emitter voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ note } 2$ $I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ note } 2$	-600 -	-650 -	-750 -820	mV mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	4.5	_	pF
C _e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = -500 \text{ mV}$; $f = 1 \text{ MHz}$	_	10	_	pF
f _T	transition frequency	$I_C = -10 \text{ mA}$; $V_{CE} = -5 \text{ V}$; $f = 100 \text{ MHz}$	100	_	_	MHz
F	noise figure BC859B; BC860B; BC859C; BC860C	$I_C = -200 \ \mu A; \ V_{CE} = -5 \ V; \ R_S = 2 \ k\Omega;$ f = 30 Hz to 15 kHz	_	_	4	dB
	noise figure BC859B; BC860B; BC859C; BC860C	$I_C = -200 \mu A; V_{CE} = -5 V; R_S = 2 k\Omega;$ f = 1 kHz; B = 200 Hz	_	_	4	dB

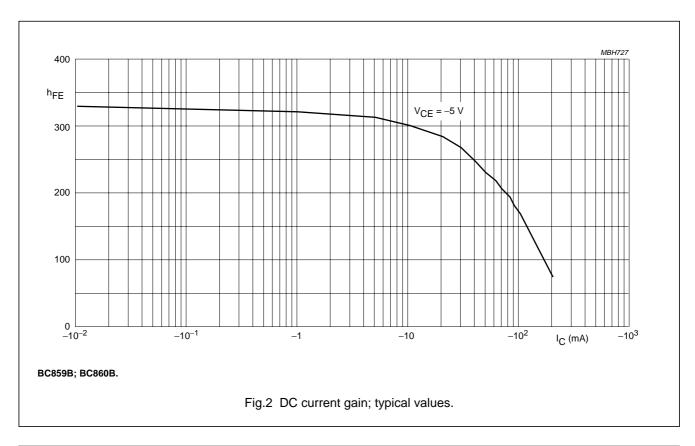
Notes

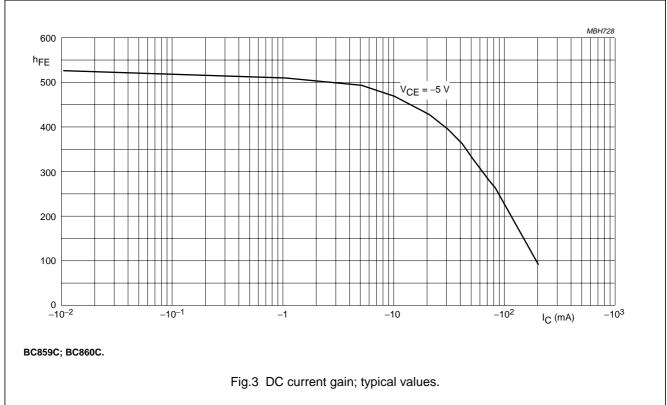
- 1. V_{BEsat} decreases by about –1.7 mV/K with increasing temperature.
- 2. V_{BE} decreases by about -2 mV/K with increasing temperature.

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PNP general purpose transistors

BC859; BC860





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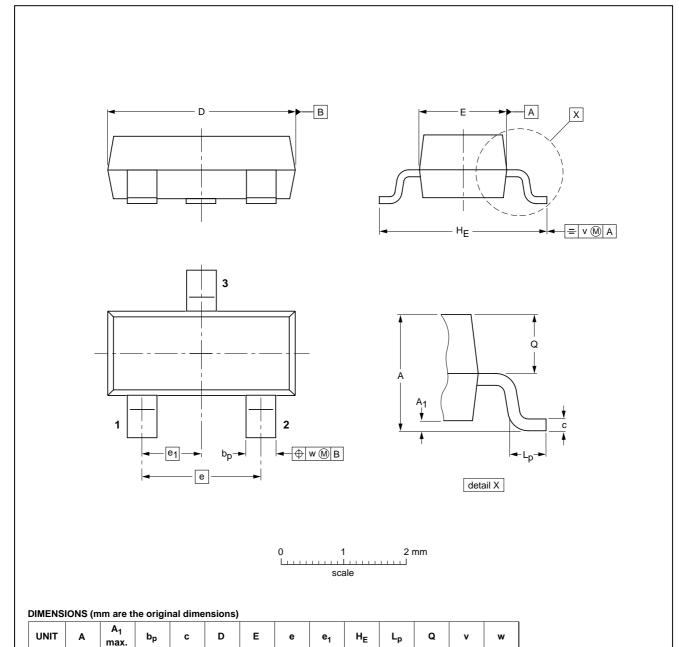
PNP general purpose transistors

BC859; BC860

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



OUTLINE	REFERENCES			EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT23						97-02-28

1.9

0.55 0.45

0.1

0.45

0.15

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0.48 0.38

0.1

1.1

mm

3.0 2.8

0.15

0.09

PNP general purpose transistors

BC859; BC860

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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PNP general purpose transistors

BC859; BC860

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